

IN THE U.S. PATENT AND TRADEMARK OFFICE

10/089799

April 2, 2002

Applicant : Seiji MATSUNAGA
Title : METHOD AND CIRCUIT FOR EMPHASIZING CONTOUR
Serial No. : Unknown Group: Unknown
Confirmation No.: Unknown
Filed : Unknown Examiner: Unknown
International Application No.: PCT/JP00/07485
International Filing Date : October 25, 2000
Atty. Docket No.: Furusawa Case 63

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Herewith is an amendment in the above-identified application.

- ☐ Statement(s) re small entity status submitted previously.
☐ Statement(s) re small entity status enclosed.
☒ No additional filing fee is required.
☐ The additional filing fee has been calculated as shown below:

For	No. Filed	No. Extra	(X) LG Entity	RATE	() SM Entity	Fee
Basic Fee				\$740.00	\$370.00	\$
Total Claims	(5 - 20 = 0)		x	\$ 18.00	x \$ 9.00	
Indep. Claims	(2 - 4 = 0)		x	\$ 84.00	x \$ 42.00	
[] Multiple Dep. Claim			+	\$280.00	+	\$140.00
* * * TOTAL FILING FEE * * *						\$ 0.00

- ☒ A Check for \$1,014.00 is enclosed to cover fees.
☒ Please credit any overpayment, or charge any additional filing fee or application processing fee required under 37 CFR 1.16 or 1.17 by this communication, to Deposit Account No. 06-1382. A duplicate copy of this sheet is enclosed.

IN DUPLICATE

TFC/smd

Encl: Listed above

Respectfully submitted,


Terrance F. Chapman Reg. No. 32 549

Express Mail Label No.: EV 040 167 848 US

JC10 Rec'd PCT/PTO 02 APR 2002

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For : METHOD AND CIRCUIT FOR EMPHASIZING CONTOUR

PCT International Application No.: PCT/JP00/07485

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(if known, see 37 CFR 1.5): Unknown

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Box PCT

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PRELIMINARY AMENDMENT CANCELING CLAIMS

Sir:

Prior to calculation of the filing fee in the above-identified application, kindly enter the following:

IN THE CLAIMS

Please amend Claim 8 as follows. A marked-up copy is enclosed herewith.

8. (Amended) The contour emphasizing circuit according to claim 6, wherein the synchronizing means is composed of the 1-dot delay circuit 11 and the 1-line delay circuit 12 for synchronizing the timings of all the 17 pixels, namely, the objective pixel C3 and the pairs of adjacent horizontal pixels C1 and C5, C2 and C4, the pairs of adjacent vertical pixels A3 and E3, B3 and D3, the pairs of adjacent rightward-rising pixels E1 and A5, D2 and B4, and the pairs of adjacent leftward-rising pixels A1 and E5, B2 and D4; the contour direction detecting stage 24 is composed of the subtracting circuit 13 for detecting the difference in the luminance between each of the pairs of the horizontal pixels C1 and C5, C2 and C4, each of the pairs of the vertical pixels A3 and E3,